

Amendment to the Claims:

The claims under examination in this application, including their current status and changes made in this paper, are respectfully presented.

1 (currently amended). A method for determining in software the effective address of instructions in a program executed on a pipelined architecture where there is no external visibility into the pipeline, the method comprising the steps of:

executing a first program;

determining that a first instruction is in the pipeline;

calculating the current effective address delay of the instruction in the pipeline;

~~finding that~~ determining whether a valid effective address for the instruction is available based on the current effective address delay of the instruction;

computing the effective address of the instruction ~~if~~ responsive to determining that a valid effective address is not available; and

reporting the effective address of the instruction.

2 (currently amended). The method of Claim 1 wherein: the step of calculating comprises subtracting the number of clock cycles that have occurred since the instruction entered the pipeline from the number of clock cycles required to compute the effective address of the instruction;

wherein the determining step of ~~finding~~ comprises determining whether ~~that~~ the current effective address delay is 0;

and wherein the step of computing is executed responsive to ~~if~~ the current effective address delay is being less than 0.